

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Seiji Funaba, et al.	Examiner:	Benjamin P. Sandvik
Serial No.:	10/672,551	Art Unit:	2826
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For:	SEMICONDUCTOR UNIT HAVING TWO DEVICE TERMINALS FOR EVERY ONE INPUT/OUTPUT SIGNAL	Dated:	October 17, 2007

Confirmation No.: 3724

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. §1.111


Sir:

In response to an Official Action mailed July 19, 2007, and in accordance with the provisions of 37 C.F.R. §1.111, Applicants submit this response for entry thereof.

CERTIFICATION OF ELECTRONIC TRANSMISSION

I hereby certify that this paper is being electronically transmitted to the Patent and Trademark Office on the date shown below.

Dated: October 17, 2007



Paul J. Esatto, Jr.